				3							1 of 1	
FORM PTO-149 NUE 3 0 2004						ATTY. DOCKET NO.			APPLICATION NO.			
		50011	DTO 440 N	16 3 0 mm 2	Ŀ	SP015.C16 (1397.028000G)			10/697,257			
		FORM	PTO-1449 N	·	-	FIRST NAMED INVENTOR: NGUYEN et al.						
INFOR	RMATIO	N DISC	LOSUR	ATEMENT	-	FILING DATE ART UNIT						
INFORMATION DISCLOSURE STATEMENTS						October 31, 2003 2183						
U.S. PATENT DOCUMENTS												
EXAMINER		DOC	UMENT		T		<del></del>			T		
INITIAL	<u></u>	NUM	BER	DATE	N/	AME	CLASS		SUB-CLASS	FILING	DATE	
	AA8											
	<del> </del>		·		┿-		_					
	AB8	1										
	AC8	<del>                                     </del>			+			-		+		
	1,00											
(1)	AD8	4,833	3,599	05/23/1989	Co	olwell et al.						
00,		<u> </u>										
101	AE8	4,974	1,154	11/27/1990	Ma	atsuo						
	1.50	- 446		00/05/4000	<del> </del>				<del>-</del>			
1/A/	AF8	5,142	2,634	08/25/1992	Fit	te et al.			<u> </u>			
	AG8	<del> </del>			╁	<del></del>			<u></u>			
	700				1						•	
	AH8				1		<del></del>	-		-		
		<u> </u>							_	<i>'</i>		
	AI8		-									
	AJ8	<del> </del>			┼			_				
	730											
	AK8	<b></b>			+		<del></del>					
		0		FOREIGN	PA	TENT DOCUMENTS				7		
EXAMINER												
INITIAL			CUMENT	DATE		COUNTRY	CLASS		SUB-CLASS	TRANSLA	NOITA	
	_	NUI	MBER	-	_			_				
_	AL7					•					Yes	
	1/15/	HEI	2-130635	05/18/1990				_		X	No Yes	
	AM7					J				^	No	
	ľ <u>.</u>										Yes	
	AN7	+		<del></del>			<u> </u>				No	
	A07									ı	Yes	
	1701			<del></del>	+-		<del></del>	-			No Yes	
1	AP7	1					]				No	
			OTHER (	including Auth	юг,	Title, Date, Pertinent Pa	ges. etc	:.)				
_			Findlay, et a	al., "HARP: A VLI	IW F	RISC Processor", Proceeding	g of 5th Al	nnı	ual European (	Computer		
	1		Conference on Advanced Computer Technology, Reliable Systems and Applica						Applications, N	May 16, 19	91, pp.	
<i>(/1</i> )	AR	<u>32</u>	368-372.									
	1	1										
1	╁		Kuga et al	"DSNS (Dynamic	cally	-hazard-resolved, Statically	-code-sch	100	tuled Nonunife	nrm		
1 , 1			Superscala	r): Yet Another Su	upe	rscalar Processor Architectu	re". Dept.	. oi	Information S	vstems.		
11/	AS	<u>32</u>	Interdiscipli	nary Graduate Sc	Yet Another Superscalar Processor Architecture", Dept. of Information Systems, ary Graduate School of Engineering Sciences, Kyushu University, Fukuoka, Japan, pp. 14-						р. 14-	
			29.								•	
<del></del>	<del> </del>	ļ	Cohuali at -	1 "A \/! !\A/ A	to cr	uro For A T O-1 - 3 P	<u> </u>		<u> </u>	<u> </u>		
$\Gamma = \Gamma \Gamma$		1	Machinen	II., "A VLIVV ARCHIT	TECTI	ecture For A Trace Scheduling Compiler", Association For Computing						
$\cup A \cup A$	АТ		Machinery (ACM), 1987, pp. 180-192.									
		<u>32</u>	1									
									1			
EXAMINER	<u> </u>	l .	·	///		<del></del>	1=			4		
EXMINER		(	L~				DATI	EC	COMSHOERED	7/A/	.	
EXAMINER: Init	al if refer	ence co	nsidered, whet	her or not citation is	s in c	conformance with MPEP 609. [	Draw line #	hro	ugh glatien if ha	in conform	ance	
and not consider	ed. Induc	le copy	of this form wij	n next communicat	tion t	to Applicant.						
305006_1.DOC			/					_	//	VOE D 11	2/02/22	
								1	√ Si	KGF Rev. 12	<b>4U3/U3</b>	

				The A							
<u> </u>					ATTY. DOCKET NO.		APPLICATION NO	Page 1 of 1			
		FORM	PTO-1449	DEC 22 2004	SP015.C16 (1397.028000G) 10/697,257						
				في	FIRST NAMED INVENTOR: NGUYEN et al.						
INFORMATION DISCLOSURE S				MEMENT NEWS	FILING DATE	•	ART UNIT				
U.S. PATENT DOCUMENTS											
EXAMINER			UMENT								
INITIAL	AA8	NUM	BEK	DATE :	NAME	CLASS	SUB-CLASS	FILING DATE			
		<u> </u>									
	AB8										
	AC8										
<i> </i> /~	AD8	4,833	,599	05/23/1989	Colwell et al.						
0	AE8	4,974,154		11/27/1990	Matsuo						
4/	AF8	5,142,634		08/25/1992	Fite et al.						
	AG8										
	AH8	<u> </u>									
	Al8										
	AJ8	ļ									
	AK8							•			
		<u></u>		FOREIGN	PATENT DOCUMENTS						
EXAMINER INITIAL		DOG	CUMENT	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION			
	-	NUMBER									
	AL7							Yes No			
	AM7	HEI	2-130635	05/18/1990	TP			X Yes			
					, , , , , , , , , , , ,			No Yes			
	AN7			<del> </del>	<del>-</del>		<u>-                                     </u>	No Yes			
	A07	ļ						No			
	AP7							Yes No			
			OTHER (	Including Auth	or, Title, Date, Pertinent	Pages, etc.	.)				
W	AR	<u>32</u>	Findlay, et a Conference 368-372.	al., "HARP: A VLI" on Advanced Co	W RISC Processor", <i>Procee</i> Imputer Technology, Reliabl	eding of 5 <sup>m</sup> An le Systems an	nual European ( d Applications, I	Computer May 16, 1991, pp.			
N	/AS	<u>32</u>	Kuga et al., "DSNS (Dynamically-hazard-resolved, Statically-code-scheduled, Nonuniform Superscalar): Yet Another Superscalar Processor Architecture", Dept. of Information Systems, Interdisciplinary Graduate School of Engineering Sciences, Kyushu University, Fukuoka, Japan, pp. 14-29.								
N	AT	<u>32</u>	Colwell et al., "A VLIW Architecture For A Trace Scheduling Compiler", Association For Computing Machinery (ACM), 1987, pp. 180-192.								
EXAMINER			M.D.	an			CONSIDERED	11/10			
EXAMINER: Initia and not considere	al if refere	ence ber	sidered whet	her or not citation is	in conformance with MPEP 60	9. Draw line the	rough citation if no	t in conformance			